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Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Sheet 1 of 1 Attorney Docket Number 22272-08030

Complete if Known	
Application No.	10/619,738
Filing Date	July 14, 2003
First Named Inventor	Sabhapathi Naidu Annamaneni
Art Unit	2812
Examiner Name	Not yet known

U.S. PATENT DOCUMENTS				
Examiner Initials*	Cite No. ¹	Document No.	Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document
Chok		US-2002/0133259 A1	09-19-2002	Hsieh
Chok		US-2002/0169842 A1	11-14-2002	Christensen et al.
Chok		US-2002/0188682 A1	12-12-2002	Jain et al.
Chok		US-2003-0233289 A1	06-14-2002	Lou et al.
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Chok		US-2003-0236718 A1	06-14-2002	Yang et al.
Chok		US-6,748,287	06-08-2004	Hagen et al.

FOREIGN PATENT DOCUMENTS				
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document
Chok	1	WO 03/107116 A2	12-24-2003	Getsilicon, Inc.
Chok	2	WO 03/107117 A2	12-24-2003	Getsilicon, Inc.
Chok	3	WO 03/107118 A2	12-24-2003	Getsilicon, Inc.

OTHER REFERENCES – NON-PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published		T ⁶
Chok	4	Effective Management of Outsourced Operations: Contending with the New Dynamics and Volatility in the Electronics Supply Chain, 05/2003, FSA. http://www.fsa.org , 6 pages.		
Chok	5	Oracle Shop Floor Management User's Guide, Release 11i, May 2001, Part No. A83717-02, 120 pages [online], [retrieved on 2004-07-14]. Retrieved from the Internet <URL: http://download-west.oracle.com/appsncl/115wsmug.pdf >.		
Chok	6	PIP® Specification, Cluster 7: Manufacturing, Segment B: Manage Manufacturing WO & WIP, 7B1: Distribute Work in Process, http://www.rosettanet.org , Feb. 28, 2003, 15 pages.		

Examiner Signature		Date Considered	2/27/07
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22272-08030/DOCS/1375865.1

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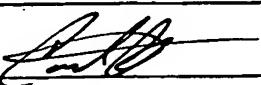
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		Document No.		
Examiner Initials*	Cite No. ¹	Number – Kind Code ² (if known)	Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document
Chop	A1	US-2004/0088074 A1	05-06-2004	Chen et al.
Chop	A2	US-2002/0188682 A1	12-12-2002	Jain et al.
Chop	A3	US-2002/0133368 A1	09-19-2002	Strutt et al.
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		US-		

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				T ⁶

OTHER REFERENCES – NON-PATENT LITERATURE DOCUMENTS				
		Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published		T ⁶
Chop	C1	International Search Report and Written Opinion, PCT/US04/22857, February 17, 2006, 7 pages.		
Chop	C2	STEVENS, S. et al., "Tighter Process Control and Reduced Cycle Times Using Off-Line Recipe Setup," IEEE/SEMI Advanced Semiconductor Manufacturing Conference, 2000, pp. 199-202.		

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22272/08030/DOCS/1604179.1

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INFORMATION DISCLOSURE
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Sheet 2

1 of 1

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Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ⁶
CRR	1	Hagen, Dr. Klaus ten et al., <u>Technical Overview: WIP Tracking IC Manufacturing workflow and Cycle-time Reduction</u> , eSilicon Corporation, Sunnyvale, USA. Available Aug. 6, 2001 at http://www.esilicon.com , 25 pages.	

Examiner Signature		Date Considered	7/27/07
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